

## TEXT SEARCH

APPLICATION # 10/604,744

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L31	32045	(impedance adj matching)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/17 13:14
L32	28	30 and (impedance adj matching)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/17 11:02
L33	35	30 and (impedance near3 match\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/17 10:18
L34	52	30 and (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/17 13:08
S2	8337	(side\$1wall or side) with (trace\$1 or wiring\$1 or circuit\$1) with (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:02
S3	794	S2 and ((trace\$1 or wiring\$1 or circuit\$1) with (width))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:50
S4	261	S2 and ((trace\$1 or wiring\$1 or circuit\$1) near3 (width))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:28
S5	53	(side\$1wall or side adj surface\$1) with (trace\$1 or wiring\$1 or circuit\$1) with (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:13

S6	1676	(side\$1wall or side adj surface\$1) with (printed adj circuit adj board or printed adj wiring adj board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:14
S7	49	S6 and ((impedance) with (match\$4 or mismatch\$4 or control\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 16:21
S8	41	S7 not S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:19
S9	1	((patterned adj circuit adj layer) with (side\$1wall)).ab,clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:26
S10	4	((patterned adj circuit adj layer\$1) with (side\$1wall\$1)).ab,clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:26
S11	27459	(side\$1wall\$1 or side adj wall\$1) with (trace\$1 or wiring\$1 or circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:27
S12	636	S11 and ((impedance) with (match\$4 or mismatch\$4 or control\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:27
S13	609	S12 not (S7 or S5 or S4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 16:22

S14	61	(trace\$1 or wiring\$1 or circuit\$1) same (varying adj width) same ((impedance) with (match\$4 or mismatch\$4 or control\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 15:53
S47	30944	(board\$1 or substrate) with (edge) with (pattern\$1 or wiring\$1 or trace\$1 or circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 19:16
S48	14622	S47 and ((edge) near3 (pattern\$1 or wiring\$1 or trace\$1 or circuit\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 19:17
S49	663	S47 and ((edge) near3 (trace\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 19:17
S50	582	S49 not (S45 or S41 or S39 or S37 or S33 or S31 or S29 or S27 or S23 or S21 or S19 or S17 or S12 or S7 or S5 or S4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 19:24
S71	330096	(differential) and (signal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/14 19:32
S73	8254	S71 and ((impedance) with (match or matching or matched))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/14 19:34
S74	1	S73 and ((trace\$1) near3 (side adj wall\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/14 19:35

S75	56	S73 and ((trace\$1) near3 (edge\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/14 19:45
S78	414298	(pwb or pcb or wiring adj board or circuit adj board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 08:35
S79	14976	S78 and ((end) adj (face\$1 or surface\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:15
S80	1054	S79 and (((end) adj (face\$1 or surface\$1)) with ((trace\$1 or wiring\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 08:38
S81	46	S80 and (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 08:43
S82	42260	S78 and ((side) adj (face\$1 or surface\$1 or wall\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 08:43
S83	3614	S82 and (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:15
S84	467	S83 and ((varying adj width) or (narrower) or (trapezoidal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:16

S85	3239	S78 and ((edge) adj (face\$1 or surface\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:15
S86	350	S85 and (impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:15
S87	51	S86 and ((varying adj width) or (narrower) or (trapezoidal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/15 09:19
S10 5	1389	(delay or interference or reflection) with (impedance adj match\$4)	US-PGPUB; USPAT	OR	OFF	2005/04/16 20:00
S10 6	404	S105 and (board\$1 or pwb or pcb)	US-PGPUB; USPAT	OR	OFF	2005/04/16 19:41
S10 7	48	S106 and ((trace or wiring or circuit) with (shape))	US-PGPUB; USPAT	OR	OFF	2005/04/16 19:51
S10 8	356	S106 not S107	US-PGPUB; USPAT	OR	OFF	2005/04/16 19:51
S10 9	1800	(pcb or pwb or board\$1 or substrate) with (impedance adj match\$4)	US-PGPUB; USPAT	OR	OFF	2005/04/16 20:30
S11 0	182	S105 and S109	US-PGPUB; USPAT	OR	OFF	2005/04/16 20:26
S11 1	2626	(pcb or pwb or board\$1 or substrate) with (impedance adj match\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 20:30
S11 2	123	S111 and (side adj wall)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 20:34